

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant	:	Songjie Xu
Appl. No.	:	09/754,406
Filed	:	January 2, 2001
For	:	METHODOLOGY AND APPLICATIONS OF TIMING DRIVEN LOGIC RESYNTHESIS FOR VLSI CIRCUITS
Examiner	:	Thomas H. Stevens
Group Art Unit	:	2123

**RESPONSE TO AUGUST 10, 2006 OFFICE ACTION AND NOVEMBER 28, 2006 NOTICE**

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated August 10, 2006 and the Office communication dated November 28, 2006, Applicant submits the following remarks for consideration in connection with the above-identified patent application.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Summary of Interview** begins on page 8 of this paper.

**Remarks/Arguments** begin on page 9 of this paper.